

Docket No. BUR920030102US1 (IBU-10-5802)

APPLICATION DATA SHEET

INVENTOR INFORMATION::

Inventor One Given Name::	Christopher B.
Family Name::	Reynolds
Postal Address Line One::	3 Lower English Settlement Road
City::	Underhill
State or Province::	Vermont
Postal or Zip Code::	05489
Citizenship Country::	United States
Inventor Two Given Name::	Sebastian T.
Family Name::	Ventrone
Postal Address Line One::	38 Butler Drive
City::	South Burlington
State or Province::	Vermont
Postal or Zip Code::	05403
Citizenship Country::	United States
Inventor Three Given Name::	Angela
Family Name::	Weil
Postal Address Line One::	6439 Andes Court
City::	Woodbridge
State or Province::	Virginia
Postal or Zip Code::	22193
Citizenship Country::	United States

ASSIGNEE INFORMATION::

ASSIGNEE::	International Business Machines Corporation
ASSIGNEE RESIDENCE::	Armonk, New York

CORRESPONDENCE INFORMATION::

Correspondence Customer Number:: 000026681

APPLICATION INFORMATION::

Title Line One:: FPGA BLOCKS WITH ADJUSTABLE POROSITY PASS
Title Line One:: THRU
Total Drawing Sheets:: 3
Formal Drawings:: Yes
Application Type:: Utility
Docket Number:: BUR920030102US1 (IBU-10-5802)

REPRESENTATIVE INFORMATION::

Representative Customer Number:: 000026681

CONTINUITY INFORMATION::

This application is a::
> Application One::
Filing Date::
Patent Number::

PRIOR FOREIGN APPLICATIONS::

Foreign Application One:: No
Filing Date::
Country::
Priority Claimed::